

# NOTICE OF RECRUITMENT



**ARRIVE TECHNOLOGIES VIETNAM CO.,LTD**

Ho Chi Minh City

**Arrive Technologies, Inc.**, with its head quarter in California, is a broadband semiconductor solutions company with a broad portfolio of highly integrated systems-on-a-chip products combining voice, data, Internet and multimedia content for worldwide telecommunications companies. Our ASICs and CodeChips™ provide flexible next-generation solutions allowing our customers to meet their growing broadband connection needs.

**Arrive Technologies Viet Nam** is a subsidiary of Arrive Technologies Inc., located at Etown Building (1), mainly responsible for researching and designing the Arrive's products.

For the company's expansion. We need to recruit fifteen (15) **more VERIFICATION DESIGN ENGINEERS.**

**Company size:**  
(Quantity of employees)

>100

**Job title:**

**Verification Design Engineer**

**Job Category:**

Hi-Tech/Telecommunications/Electric-Electronics/IT

**Job Description:**

- Define verification plan, coverage plan based on Chip functionalities, standard, and application.
- Be responsible for Verification sign-off of various transport chips.
- **For simulation field:** Define/implement Block/System level test-bench based on the latest advanced verification methodologies (OVM, UVM ...). Define/implement test cases following verification plans.
- **For Emulation/Validation field:** Verify chips at system level. Define/implement automation framework. Define/implement test cases following verification plans.

**\*\*Working conditions:**

- To work at Etown Building, PCs with strong configuration, modern lab (telecom equipments and embedded systems in MAN/WAN networks, etc...).
- Very competitive salary.
- Dinner & parking allowance at Etown since probation time
- Other allowances: transportation, lunch, health insurance at international hospitals located in Viet Nam (such as Phap Viet, Colombia, Victoria Health Care, An Sinh, ...)

**Job Requirement:**

- Bachelor of engineering, major in IC-Design/ Telecommunication/Electric-Electronics/IT/Software **with GPA at least 7.0/10**
- Telecommunication knowledge about Ethernet, MPLS, TCP/IP...
- Familiar with Linux/Unix working environment.



- Good problem solving & communication skills.
- Good ability to read & understand telecommunication standards/ books or write reports in English.

**Preferred:**

- Familiar with design and verification tools: Cadence/Mentor Graphic, VCS...
- Expertise in chip design/verification languages: Verilog, System Verilog, C, C++...
- Familiar with Verification Methodologies: OVM, UVM...
- Familiar with script languages: Python, Perl, C-Shell, Bash-Shell, TCL...

\*\* Interest candidates please send the resume and university score transcript via email [jobs@atvn.com.vn](mailto:jobs@atvn.com.vn) with subject "Verification Design Engineer " or directly to the company address: Arrive Technologies Vietnam, 10th Fl, Etown Building (1), 364 Cong Hoa, Tan Binh Dist., HCM City, Vietnam before 25th of November, 2014.

**Preferred Language:** English

**Qualification:** Bachelor

**Experience:** Not require

**Salary:** Very competitive

**Job Type:** Full time

**Contact Person:** Ms. Thuan

**Email for applications:** [jobs@atvn.com.vn](mailto:jobs@atvn.com.vn)

**District:** Tan Binh

**Address:** 10 Fl., Etown Building (1), 364 Cong Hoa

**City:** Ho Chi Minh City

**Country:** Viet nam

**Telephone Number:** 08 38127479

**Fax Number:** 08 38121484

**Company Website:** [www.arrivetechnologies.com](http://www.arrivetechnologies.com)



**Ho Chi Minh City, Oct 20, 2014**  
**DEPUTY GENERAL DIRECTOR**

**TRAN THI NGOC DIEP**